

## CLAIMS:

1. A semiconductor processing method, comprising:

providing a semiconductor substrate comprising a layer comprising at least one metal in elemental or metal alloy form, the metal comprising an element selected from the group consisting of platinum, ruthenium, rhodium, palladium, iridium, and mixtures thereof; and

etching at least a portion of the layer in a halogenide, ozone and H<sub>2</sub>O comprising ambient.

2. The method of claim 1 wherein the halogenide is provided to the portion of the layer in a liquid solution, and the ozone is provided to the portion of the layer in gaseous form.

3. The method of claim 1 wherein the ambient comprises a temperature from about 5°C to about 200°C.

4. The method of claim 1 wherein the ambient comprises a temperature of at least 20°C.

5. The method of claim 1 wherein the ambient comprises a temperature of at least 60°C.

6. The method of claim 1 wherein the at least one metal is in elemental form.

7. The method of claim 1 wherein the at least one metal is in metal alloy form.

8. The method of claim 1 wherein the at least one metal is in metal alloy form and includes at least two metals selected from the group consisting of platinum, ruthenium, rhodium, palladium and iridium.

9. The method of claim 1 wherein the at least one metal is in metal alloy form and includes at least one metal selected from the group consisting of platinum, ruthenium, rhodium, palladium and iridium, and at least one other not selected from the group consisting of platinum, ruthenium, rhodium, palladium and iridium.

10. The method of claim 1 wherein the layer consists essentially of said at least one metal in elemental or metal alloy form.

11. The method of claim 1 wherein the element comprises platinum.

12. The method of claim 1 wherein the element comprises ruthenium.

13. The method of claim 1 wherein the element comprises rhodium.

14. The method of claim 1 wherein the element comprises palladium.

15. The method of claim 1 wherein the element comprises iridium.

16. The method of claim 1 wherein the halogenide is provided to the portion of the layer in a liquid salt solution.

17. The method of claim 1 wherein the halogenide is provided to the portion of the layer in a liquid salt solution, the liquid salt solution comprising at least one of sodium chloride, potassium chloride and calcium chloride.

18. The method of claim 1 wherein the halogenide is provided to the portion of the layer in a liquid solution, the liquid solution comprising HCl.

19. The method of claim 1 wherein the etching ambient comprises atmospheric pressure.

20. The method of claim 1 wherein the etching ambient comprises a pressure greater than atmospheric.

21. A semiconductor processing method, comprising:

providing a semiconductor substrate comprising a layer comprising at least one metal in elemental or metal alloy form, the metal comprising an element selected from the group consisting of platinum, ruthenium, rhodium, palladium, iridium, and mixtures thereof; and

wet etching at least a portion of the layer using an aqueous halogenide ion containing liquid solution and ozone.

22. The method of claim 21 wherein the wet etching is conducted at a temperature of at least 20°C.

23. The method of claim 21 wherein the wet etching is conducted at a temperature of at least 60°C.

24. The method of claim 21 wherein the liquid solution comprises HCl.

25. The method of claim 21 wherein the liquid solution comprises HCl, and the wet etching is conducted at a temperature of at least 60°C.

26. The method of claim 21 wherein the liquid solution comprises at least one of sodium chloride, potassium chloride and calcium chloride.

27. The method of claim 21 wherein the liquid solution and the ozone are provided to the portion of the layer from separate emitters.

28. The method of claim 21 wherein the liquid solution and the ozone are provided to the portion of the layer from a common emitter.

29. The method of claim 21 wherein the element comprises at least one of platinum and rhodium.

30. A semiconductor processing method, comprising:

providing a semiconductor substrate comprising a layer comprising at least one metal in elemental or metal alloy form, the metal comprising an element selected from the group consisting of platinum, ruthenium, rhodium, palladium, iridium, and mixtures thereof; and

spraying an aqueous halogenide ion containing liquid solution onto the layer and providing gaseous ozone onto the layer during the spraying under conditions effective to etch at least a portion of the layer from the substrate.

31. The method of claim 30 wherein the element comprises at least one of platinum and rhodium.

32. The method of claim 30 wherein the liquid solution and the ozone are provided onto the layer from separate emitters.

33. The method of claim 30 wherein the liquid solution and the ozone are provided to the portion of the layer from a common emitter.

34. The method of claim 30 wherein the wet etching is conducted at a temperature of at least 20°C.

35. The method of claim 30 wherein the wet etching is conducted at a temperature of at least 60°C.

36. The method of claim 30 wherein the liquid solution comprises HCl.

37. The method of claim 30 wherein the liquid solution comprises HCl, and the wet etching is conducted at a temperature of at least 60°C.

38. The method of claim 30 wherein the liquid solution comprises at least one of sodium chloride, potassium chloride and calcium chloride.

39. The method of claim 30 further comprising spinning the substrate during the spraying.

40. A semiconductor processing method, comprising:

forming a layer comprising at least one metal in elemental or metal alloy form over at least one side of a semiconductor wafer, the metal comprising an element selected from the group consisting of platinum, ruthenium, rhodium, palladium, iridium, and mixtures thereof; the wafer having a central portion surrounded by a peripheral portion;

forming masking material over the central portion of the layer while leaving the peripheral portion outwardly exposed; and

etching the peripheral exposed portion of the layer from the wafer using a halogenide, ozone and  $H_2O$  comprising ambient while the masking material is received over the central portion.

41. The method of claim 40 wherein the masking material comprises organic resist.

42. The method of claim 40 wherein the masking material comprises a photoresist.

43. The method of claim 40 wherein the masking material is inorganic.

44. The method of claim 40 wherein the semiconductor wafer comprises side edges and another side opposite the one side, the method further comprising forming said layer over at least a portion of the side edges and over at least a portion of the another side, the etching removing all of said layer from the side edges and the other side.

45. The method of claim 40 wherein the etching comprises wet etching using an aqueous halogenide ion containing liquid solution and gaseous ozone.

46. The method of claim 40 wherein the etching comprises spraying an aqueous halogenide ion containing liquid solution onto the masking material and the peripheral exposed portion and providing gaseous ozone onto the masking material and the peripheral exposed portion during the spraying.

47. The method of claim 40 wherein the etching is conducted isotropically effective to etch some of the layer beneath the masking material.

48. The method of claim 40 wherein the etching is conducted at a temperature of at least 20°C.

49. The method of claim 40 wherein the etching is conducted at a temperature of at least 60°C.



50. The method of claim 40 wherein the etching comprises a halogenide containing liquid solution comprising HCl.

51. The method of claim 40 wherein the etching comprises a chloride containing liquid solution comprising at least one of sodium chloride, potassium chloride and calcium chloride.

52. A method of forming a capacitor comprising:

forming first and second capacitor electrode layers separated by a capacitor dielectric region over a substrate, at least one of the capacitor electrode layers comprising at least one metal in elemental or metal alloy form, the metal comprising an element selected from the group consisting of platinum, ruthenium, rhodium, palladium, iridium, and mixtures thereof;

forming masking material over a first portion of said at least one capacitor electrode layer while leaving a second portion of said at least one capacitor electrode layer exposed; and

etching the exposed second portion of said at least one capacitor electrode layer using a halogenide, ozone and H<sub>2</sub>O comprising ambient while the masking material is received over the first portion effective to form a desired pattern of said at least one capacitor electrode layer.

53. The method of claim 52 wherein the etching comprises wet etching using an aqueous halogenide ion containing liquid solution and gaseous ozone.

54. The method of claim 52 wherein the etching comprises spraying an aqueous halogenide ion containing liquid solution onto the masking material and the exposed second portion and providing gaseous ozone onto the masking material and the exposed second portion during the spraying.

55. The method of claim 52 wherein the etching is conducted isotropically effective to etch some of said at least one capacitor electrode layer beneath the masking material.

56. The method of claim 52 wherein the etching is conducted at a temperature of at least 20°C.

57. The method of claim 52 wherein the etching is conducted at a temperature of at least 60°C.

58. The method of claim 52 wherein the etching comprises a chloride containing liquid solution comprising HCl.

59. The method of claim 52 wherein the etching comprises a chloride containing liquid solution comprising at least one of sodium chloride, potassium chloride and calcium chloride.